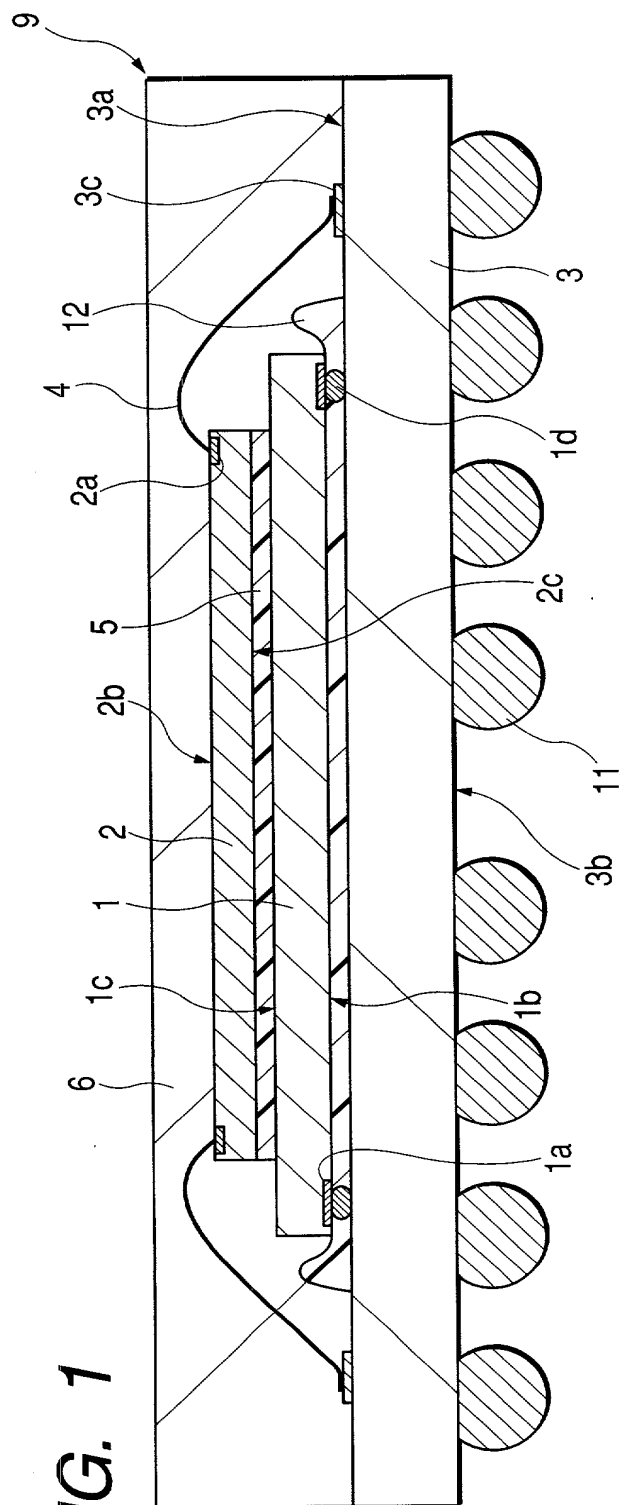
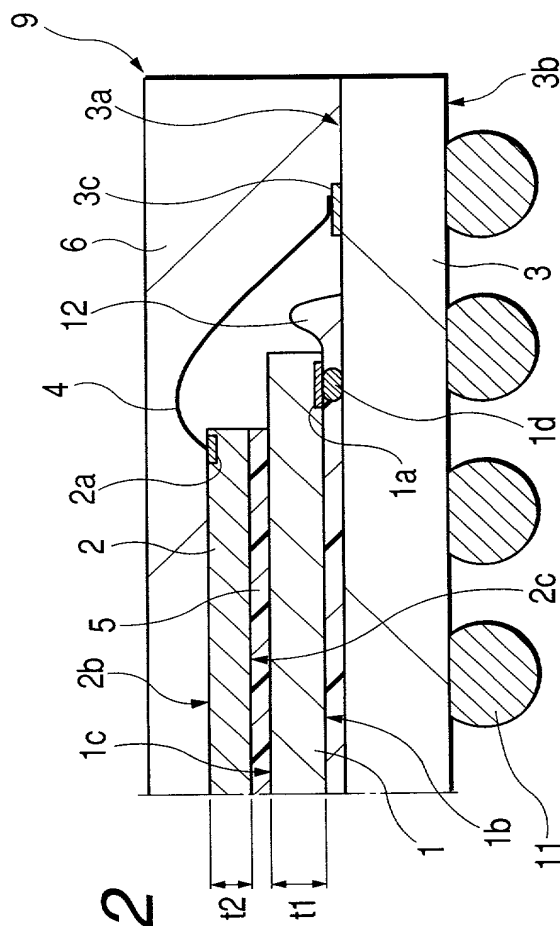
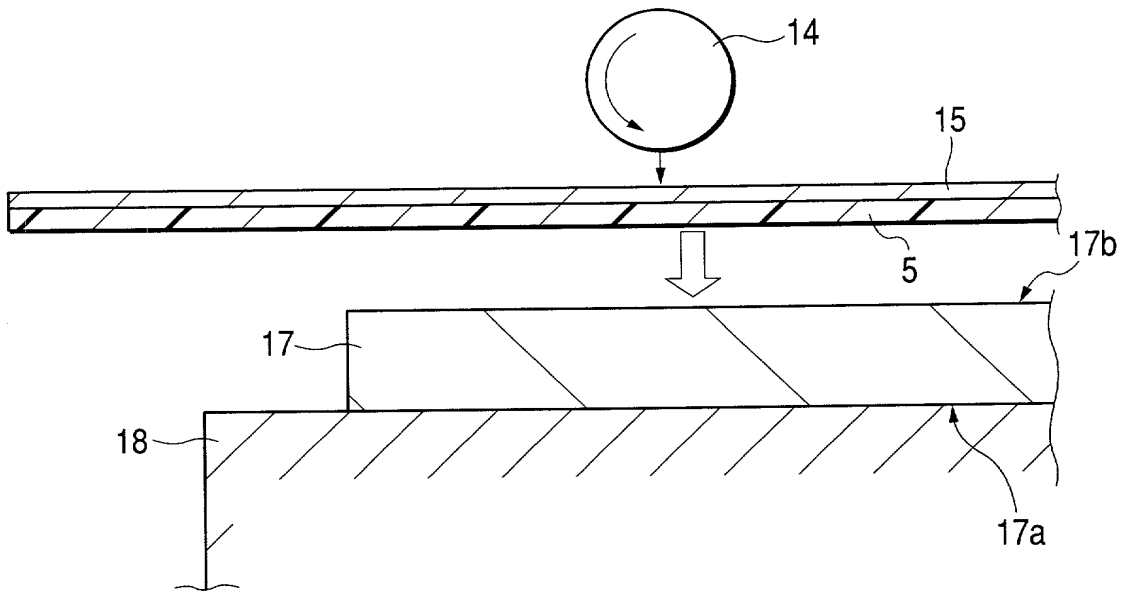
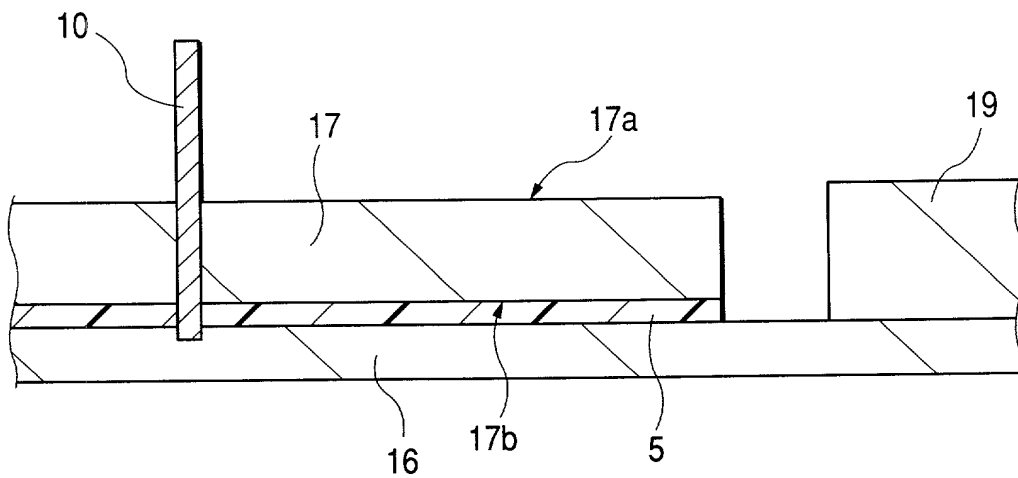


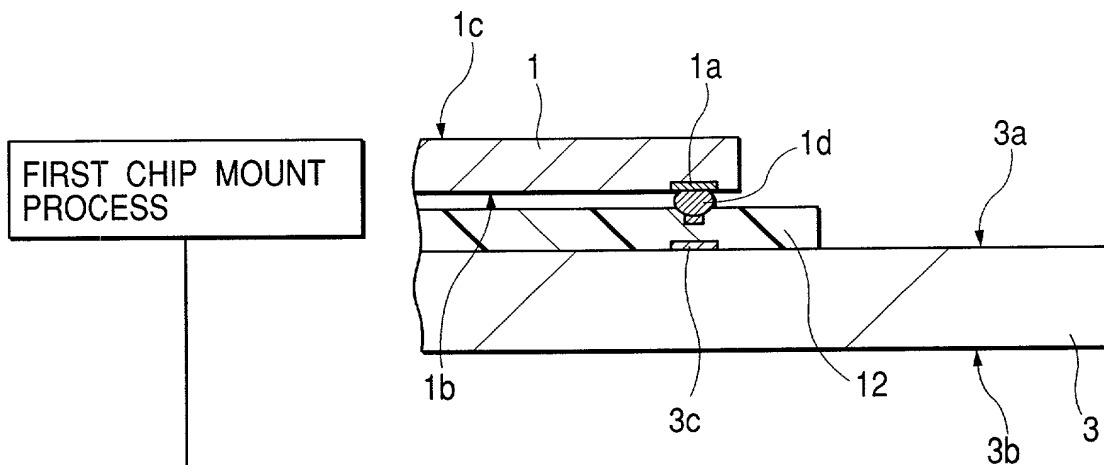
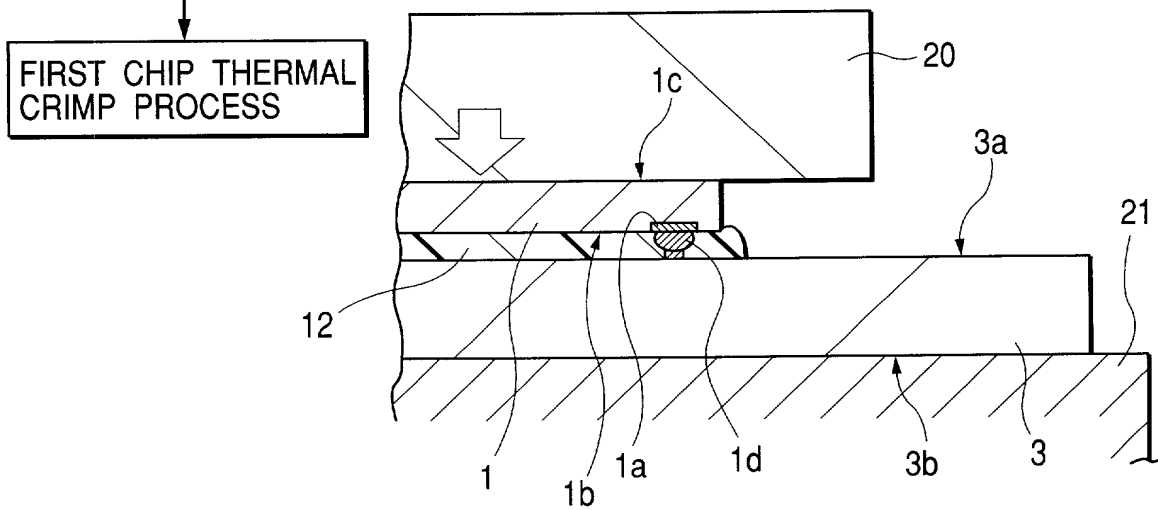
**FIG. 1**



**FIG. 2**



**FIG. 3****FIG. 4**

*FIG. 5(a)**FIG. 5(b)*

**FIG. 6(b)**

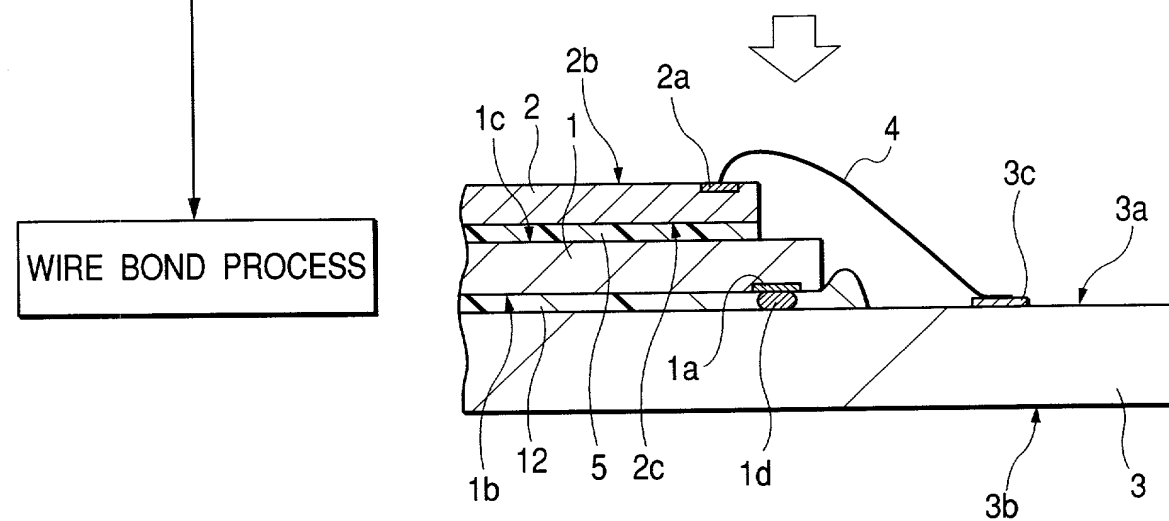


FIG. 7

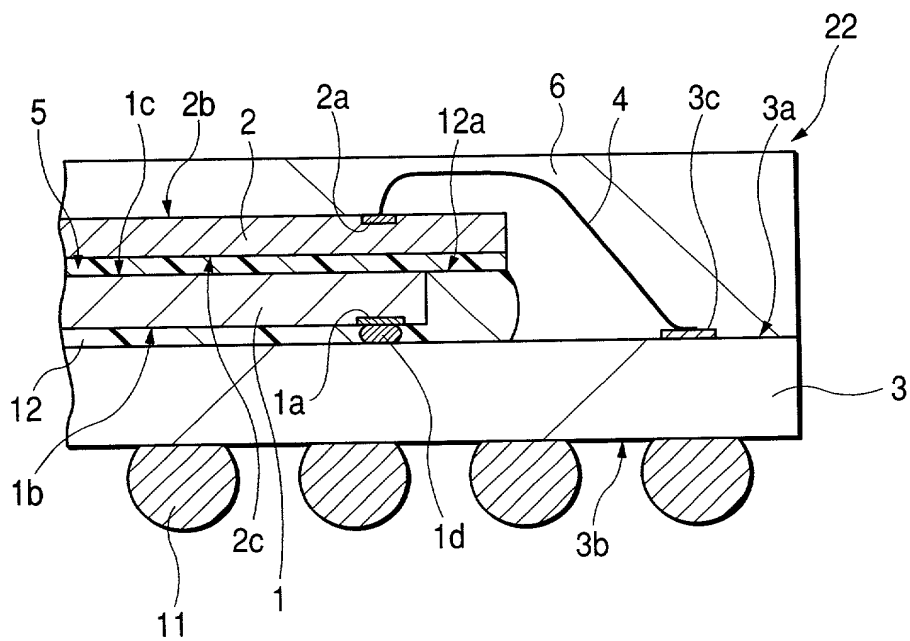
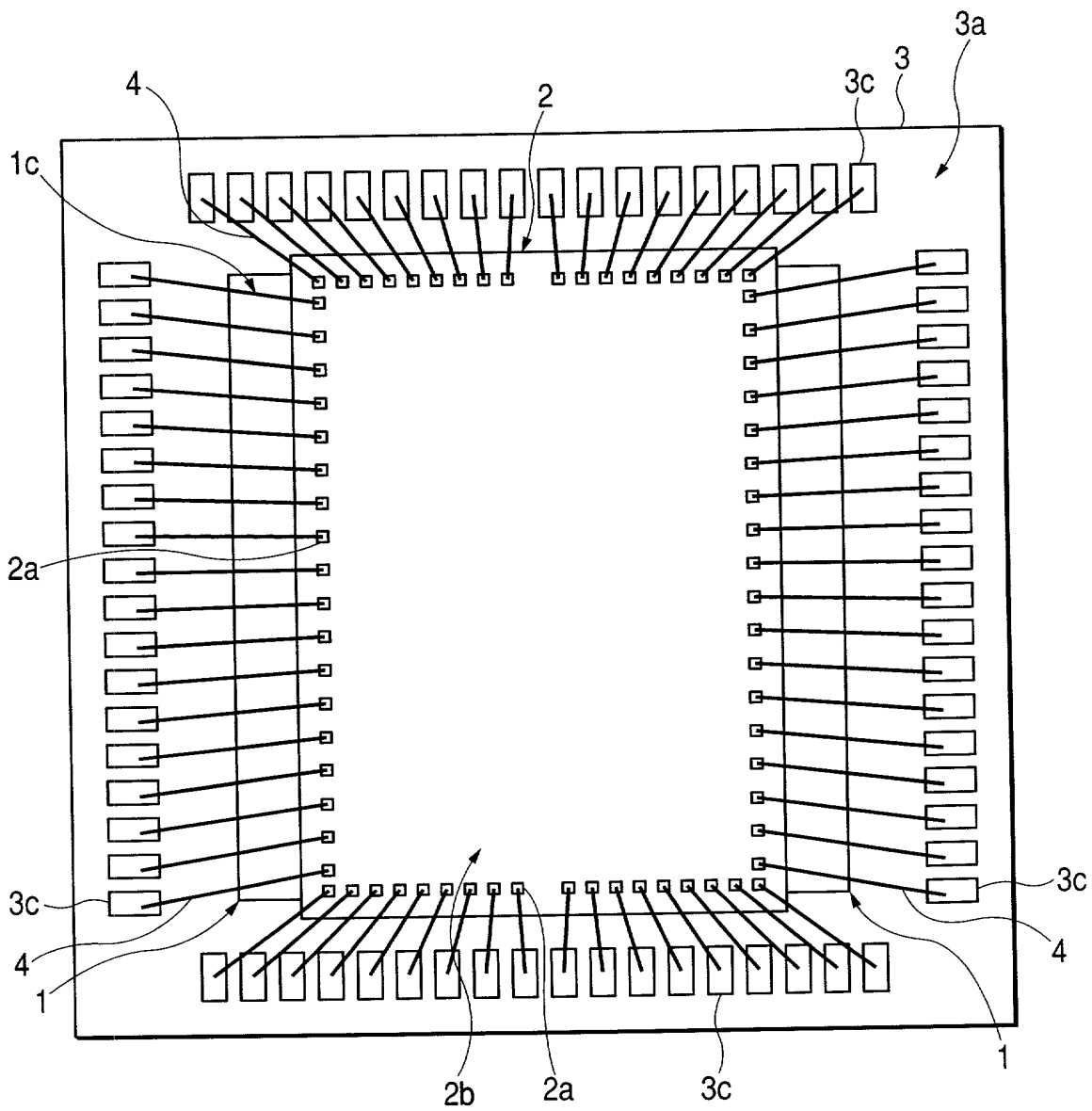


FIG. 8



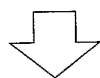
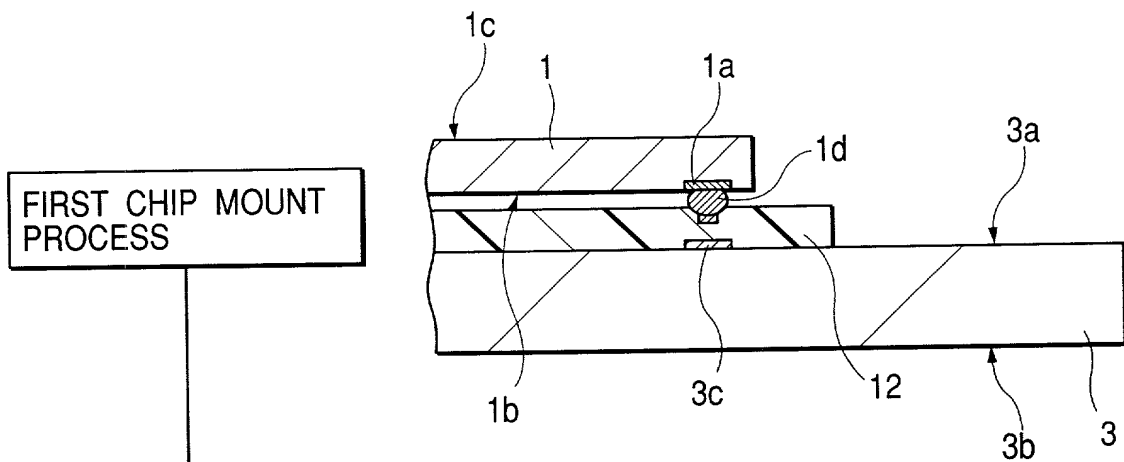
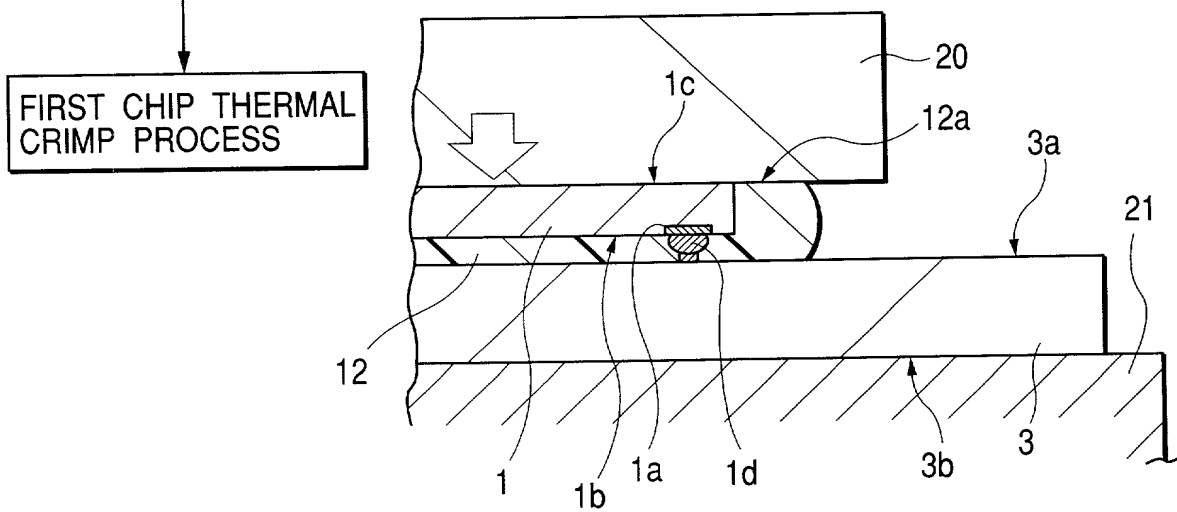
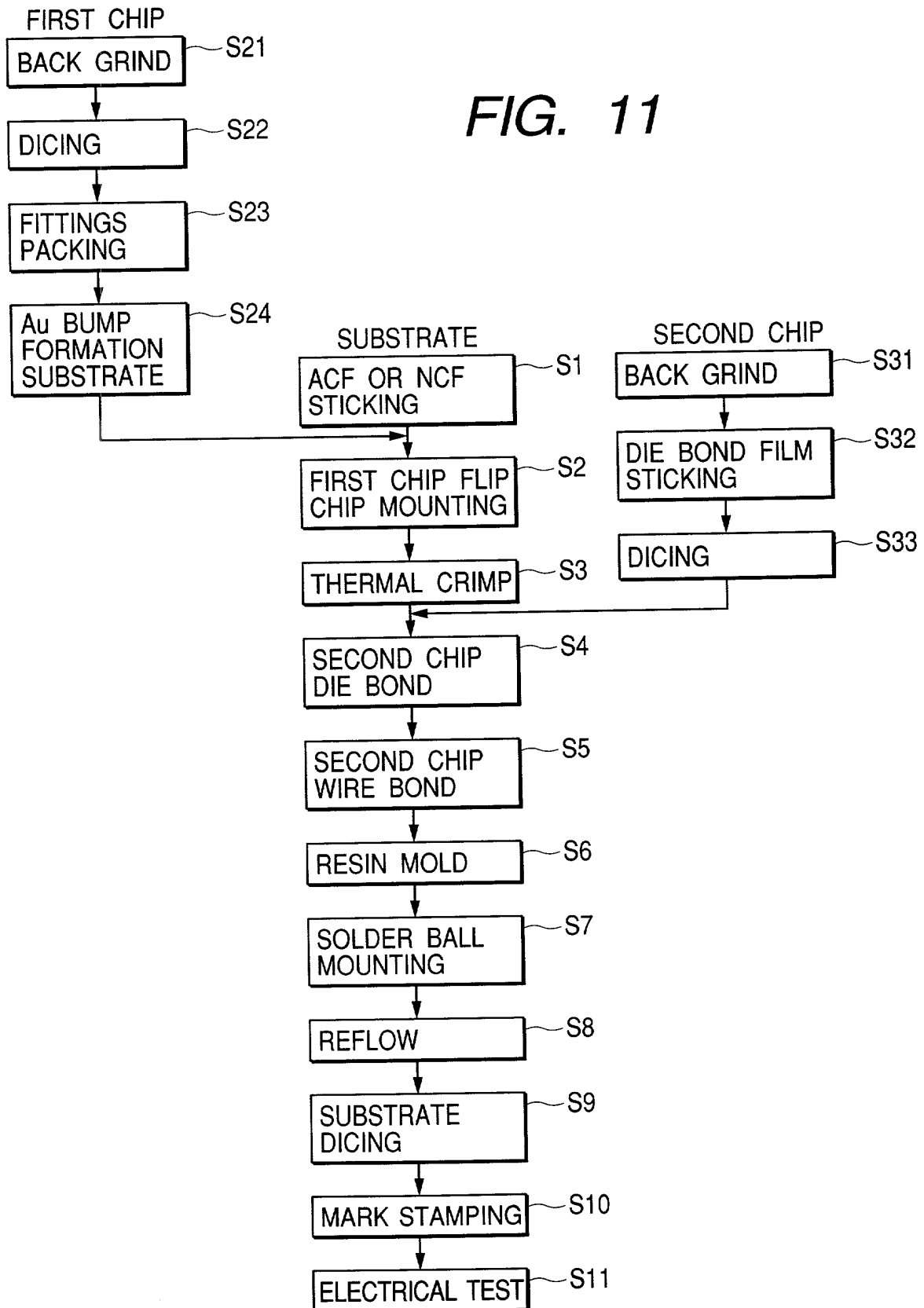
*FIG. 9(a)**FIG. 9(b)*





FIG. 11



**FIG. 12**

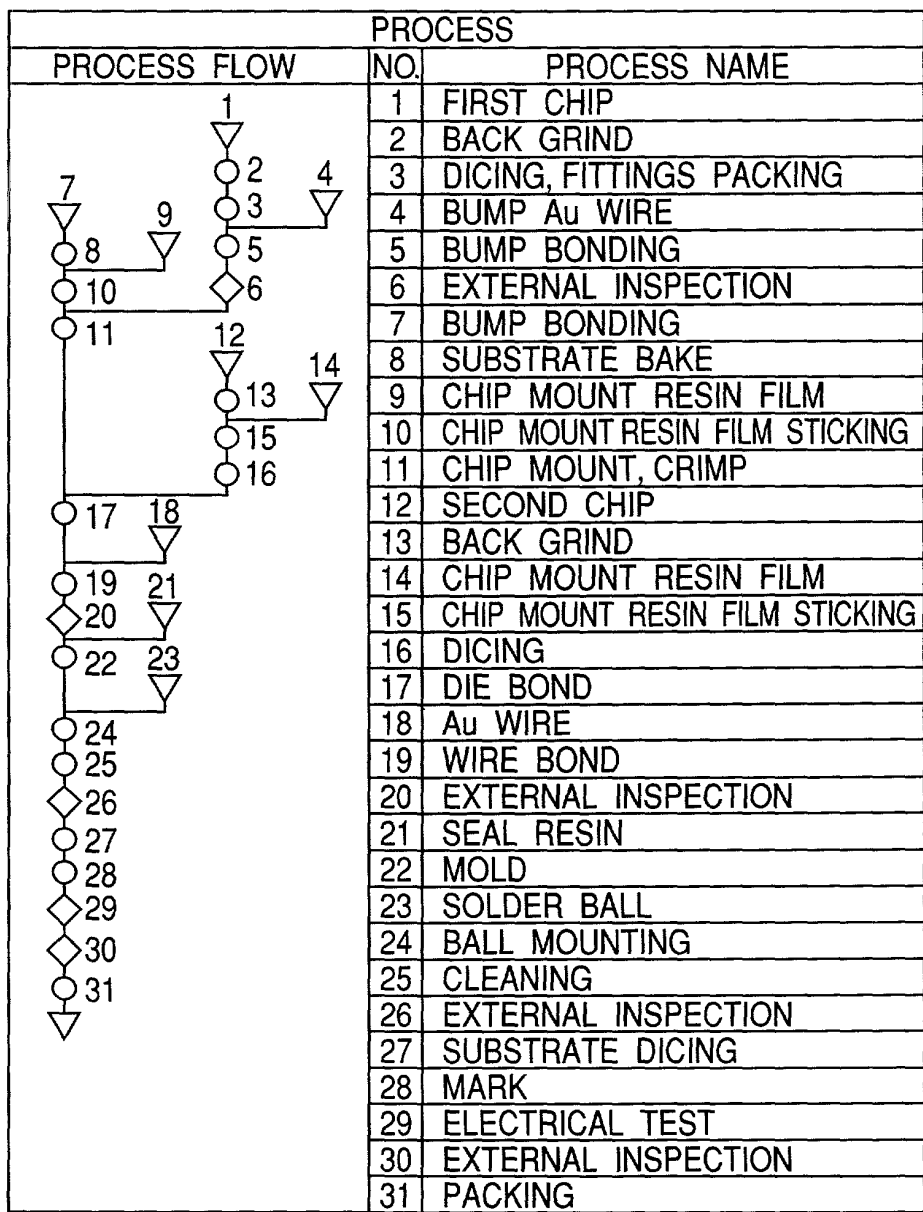


FIG. 13

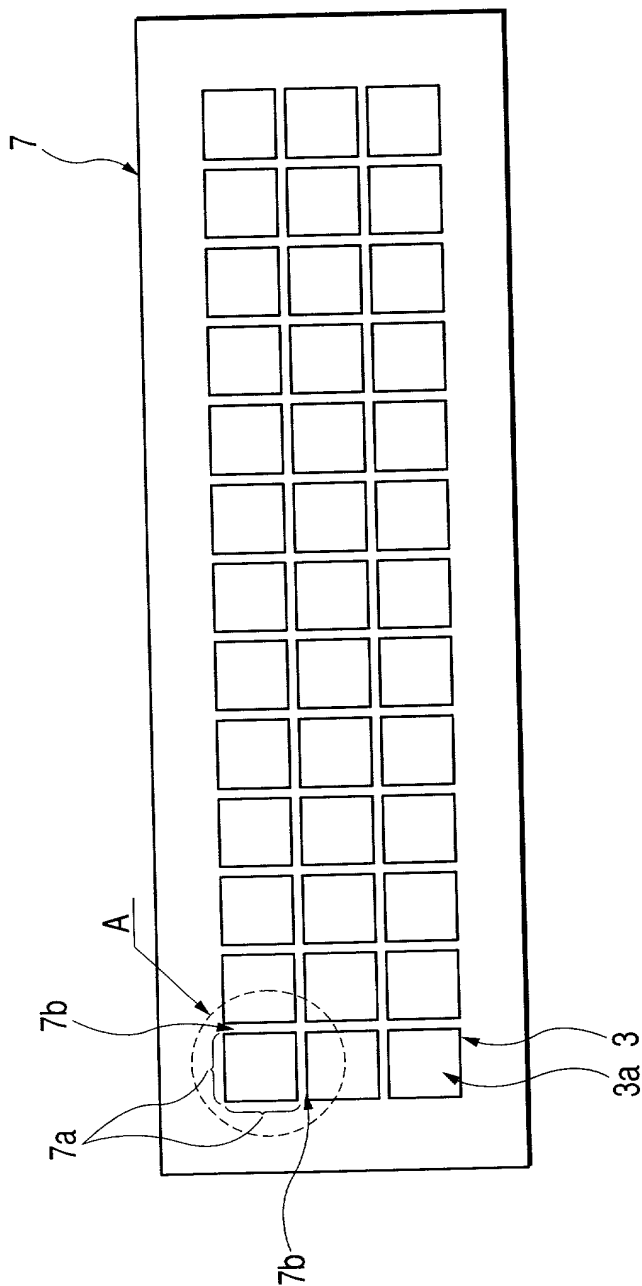


FIG. 14(a)

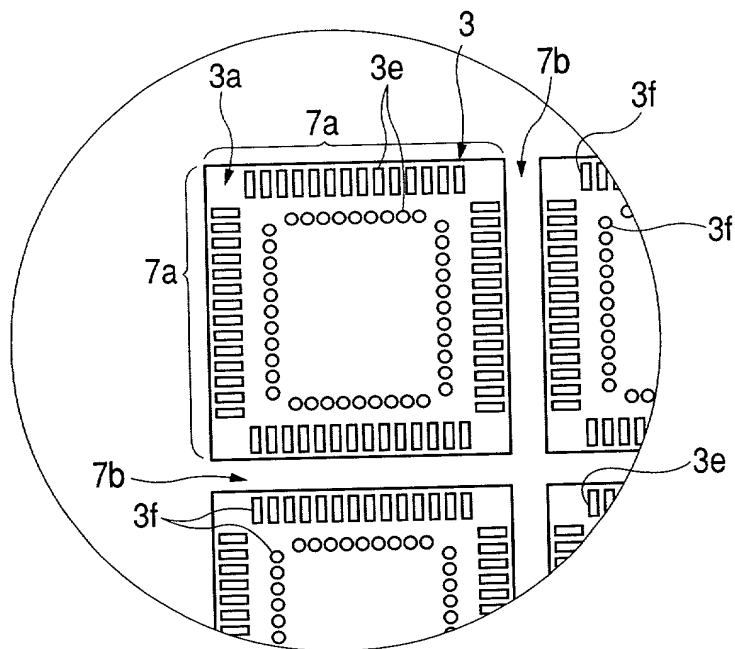
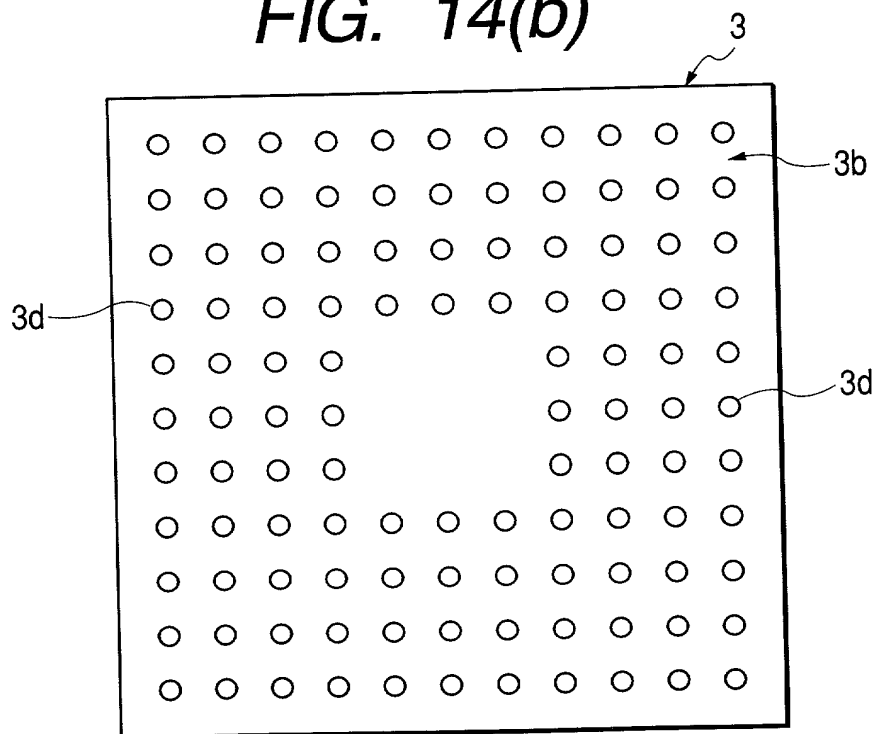
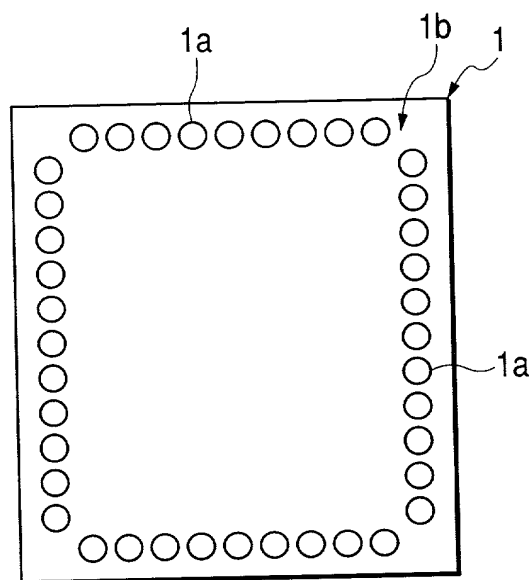


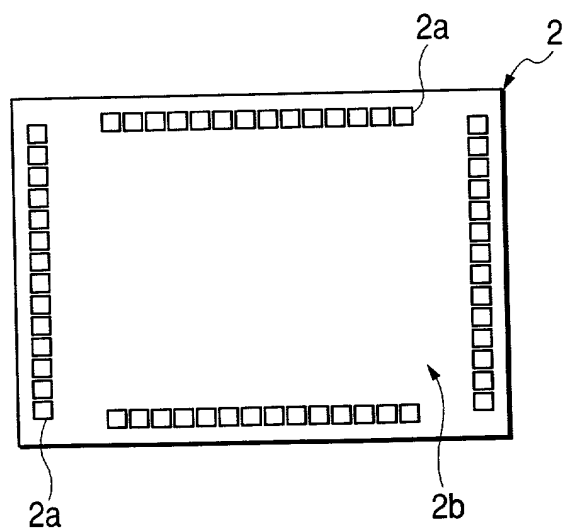
FIG. 14(b)



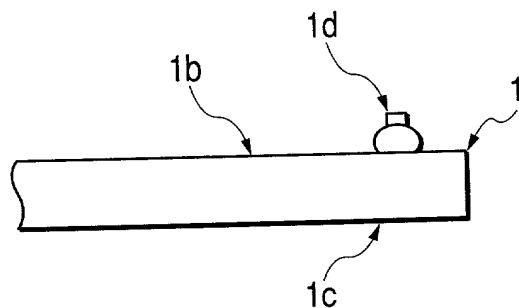
**FIG. 15(a)**



**FIG. 15(b)**



*FIG. 16(a)*



*FIG. 16(b)*

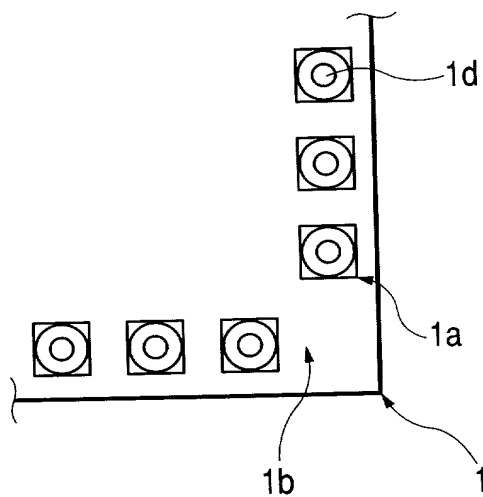


FIG. 17

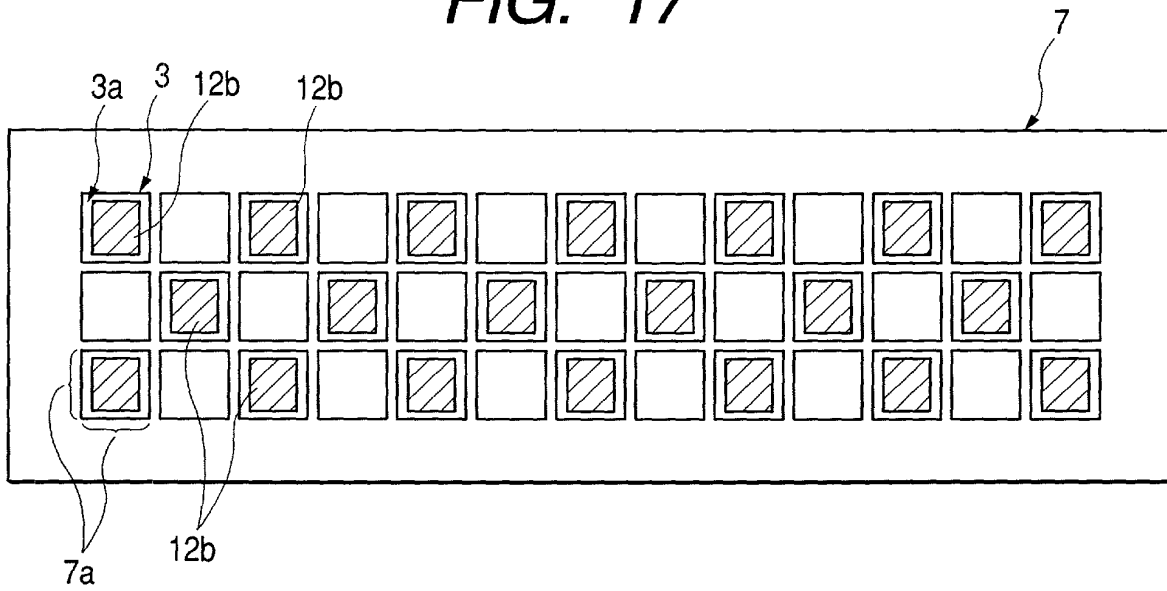


FIG. 18(a)

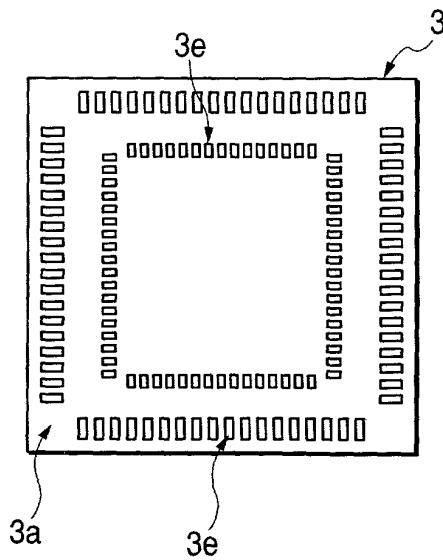


FIG. 18(b)

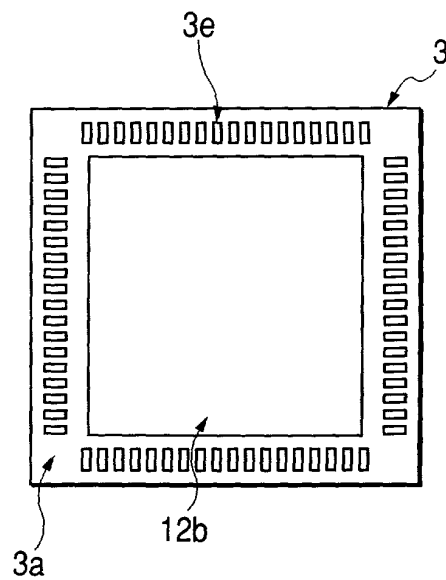


FIG. 19(a)

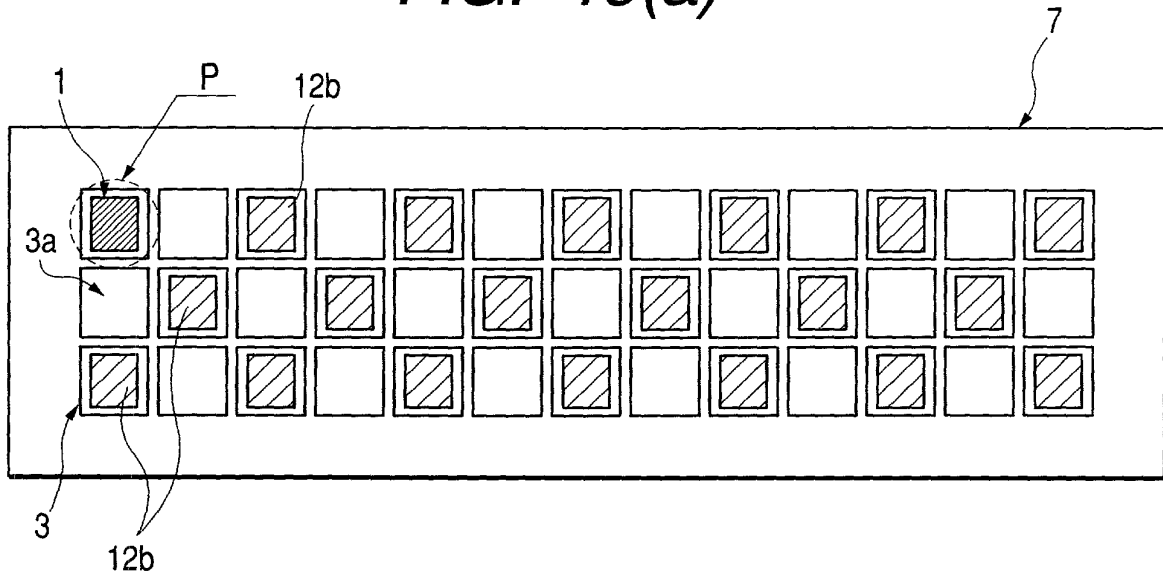


FIG. 19(b)

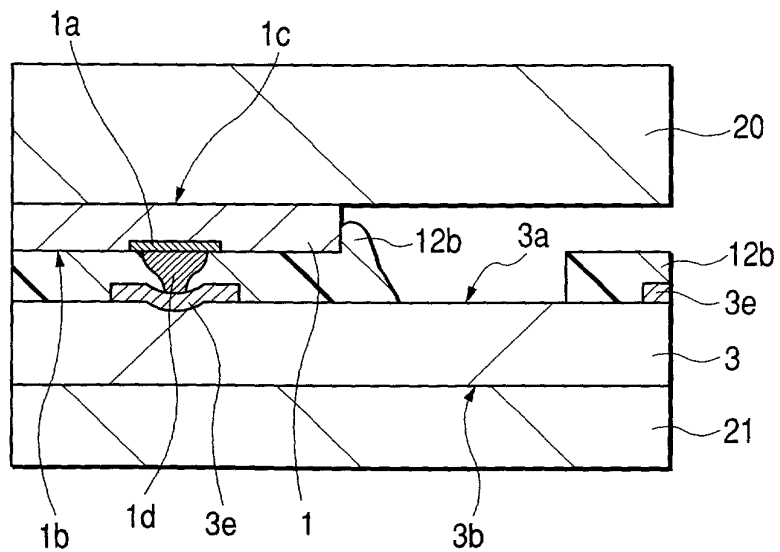




FIG. 20(a)

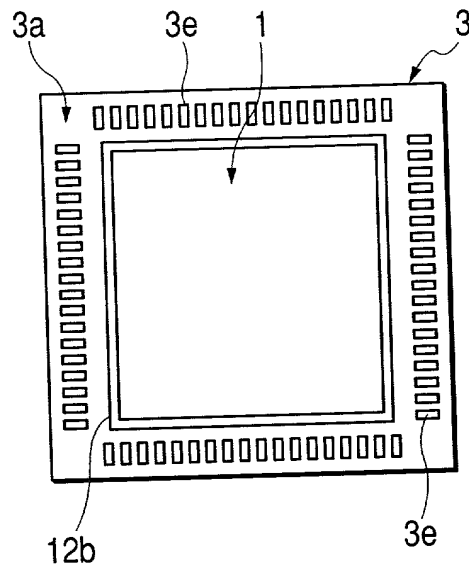


FIG. 20(b)

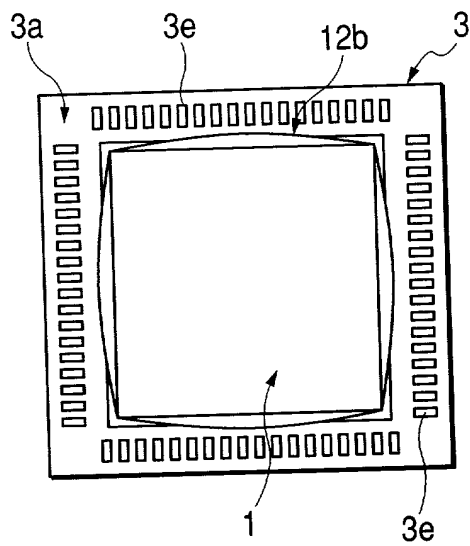


FIG. 21

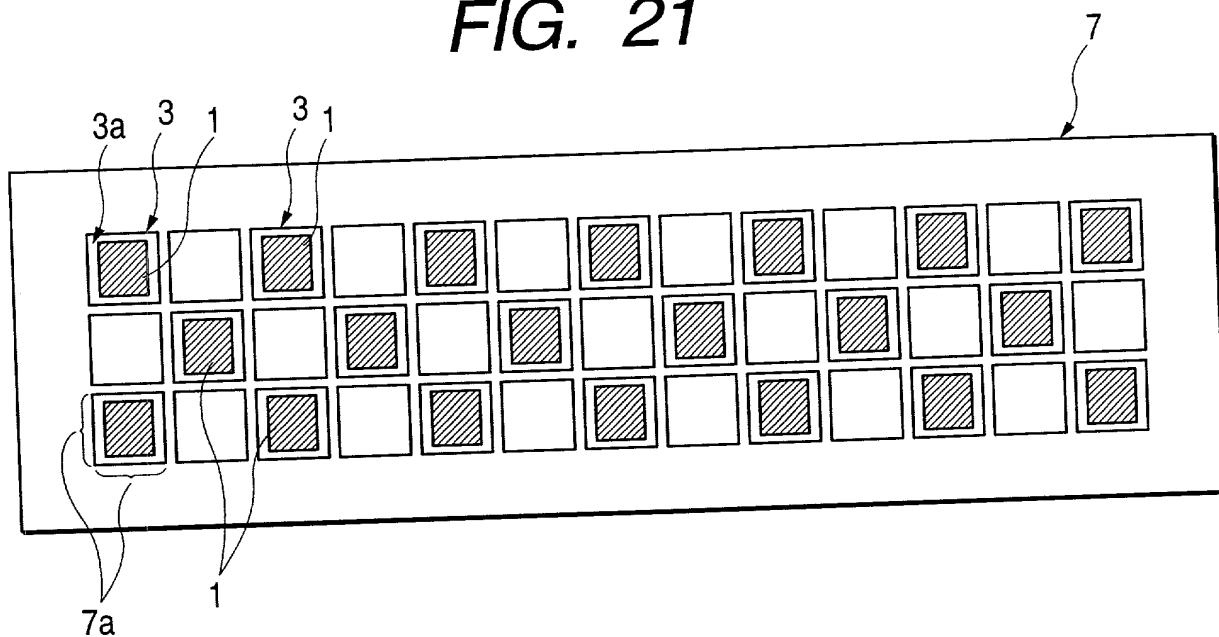


FIG. 22

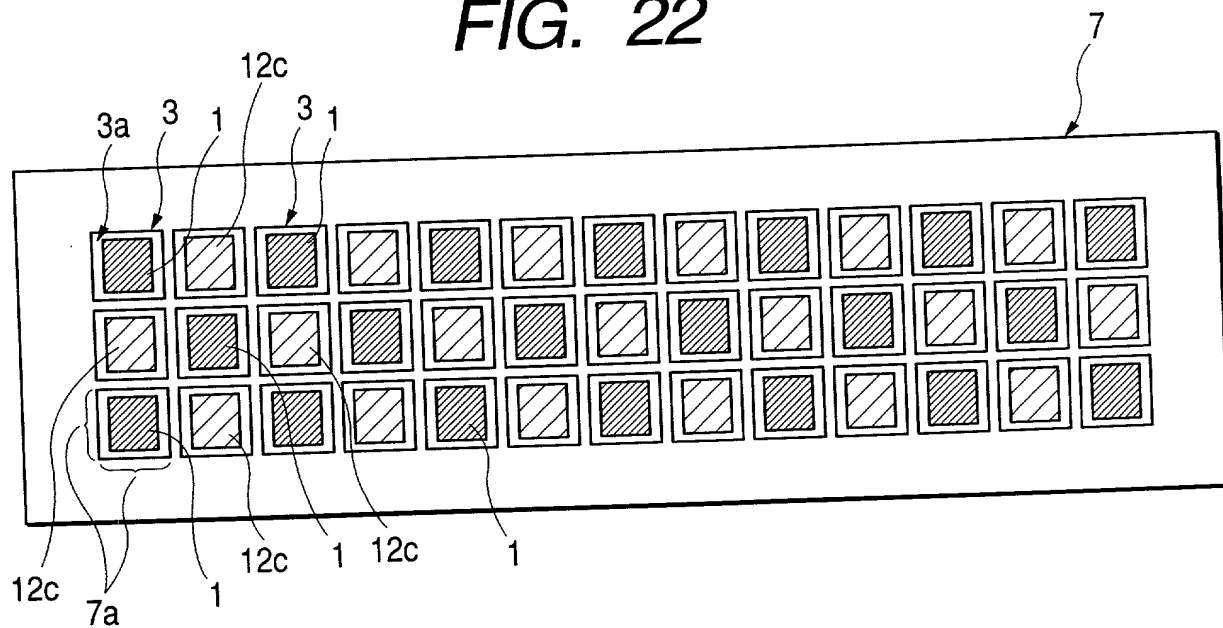


FIG. 23(a)

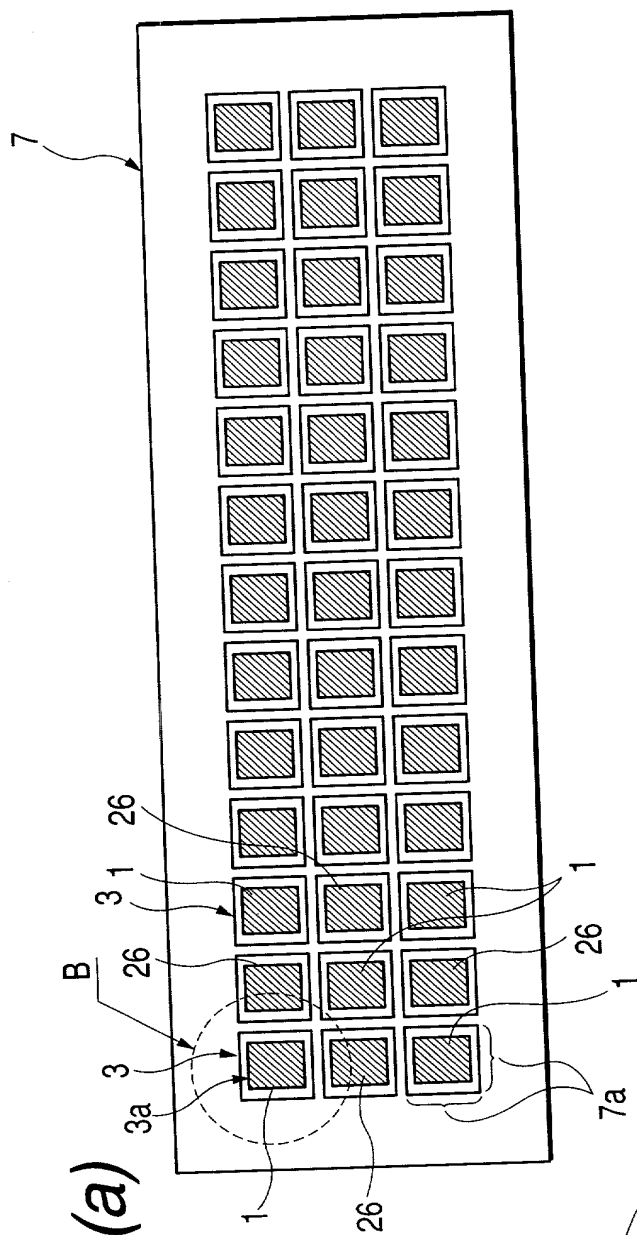


FIG. 23(b)

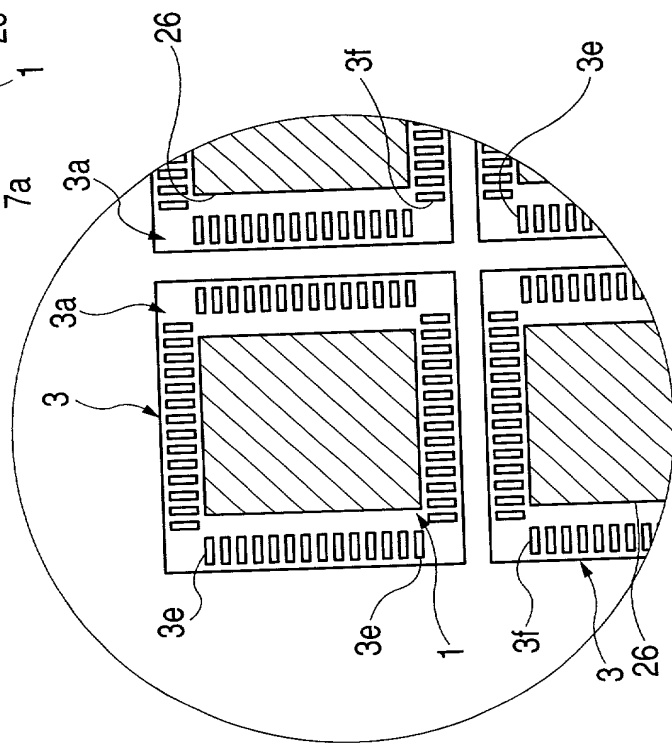


FIG. 24

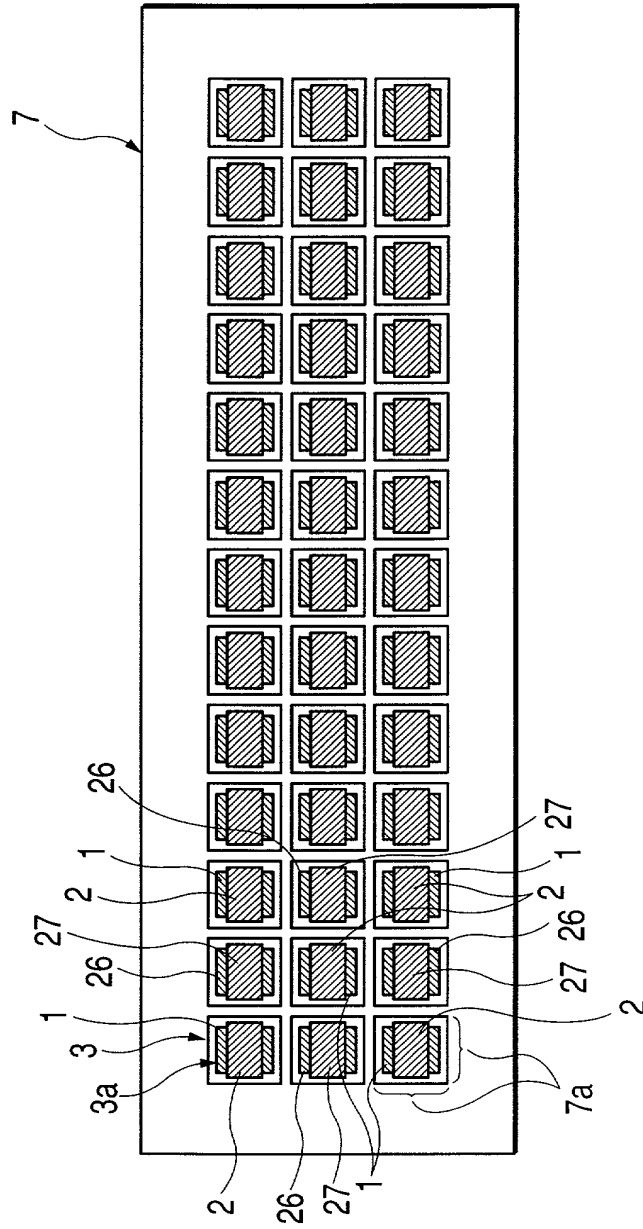


FIG. 25(a)

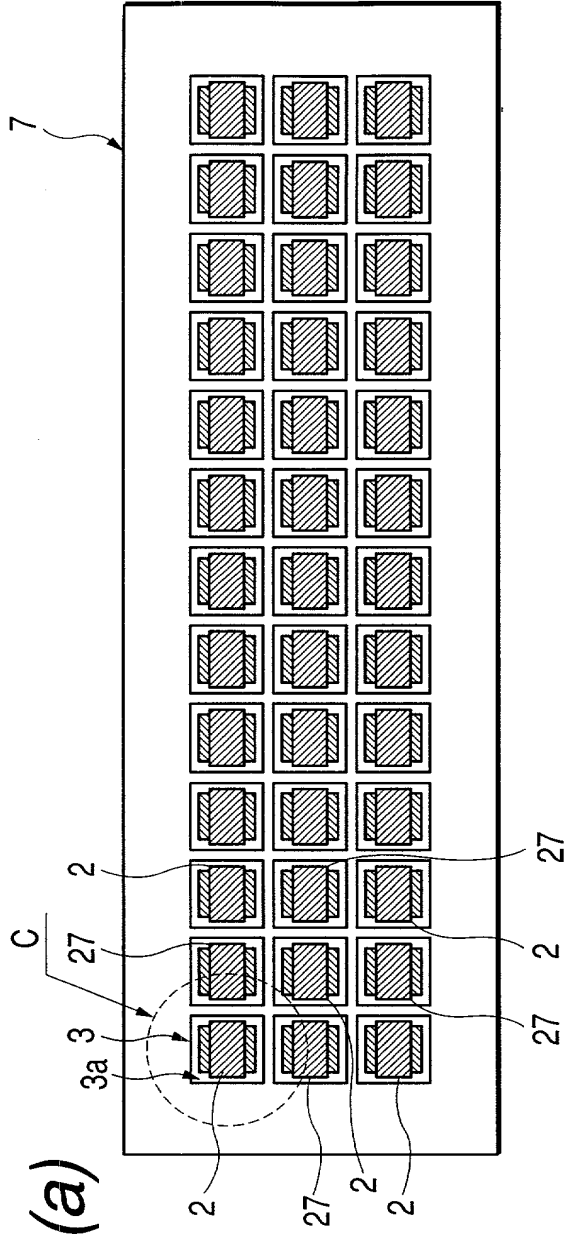


FIG. 25(b)

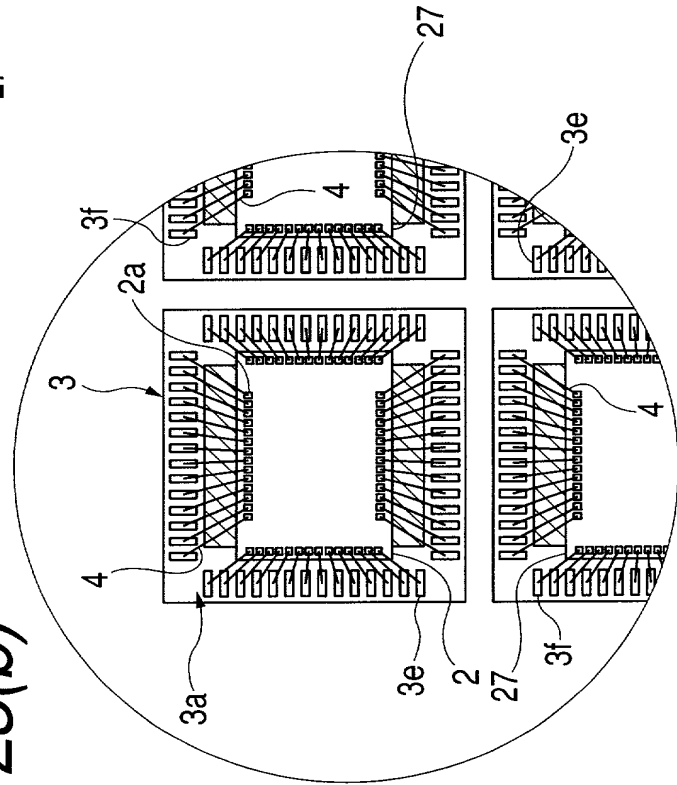


FIG. 26(a)

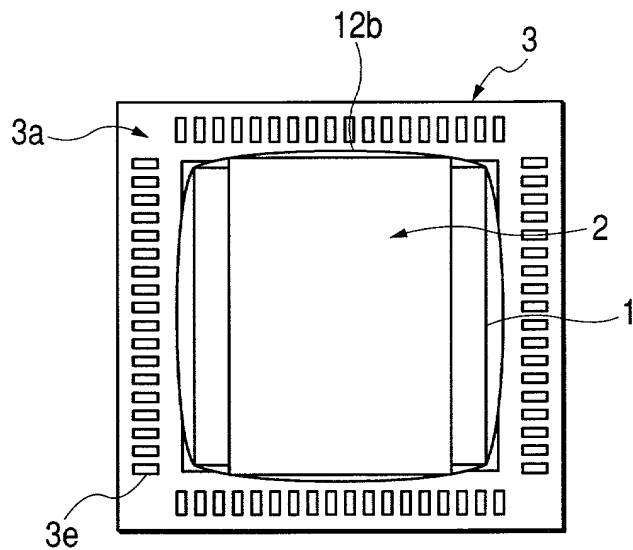
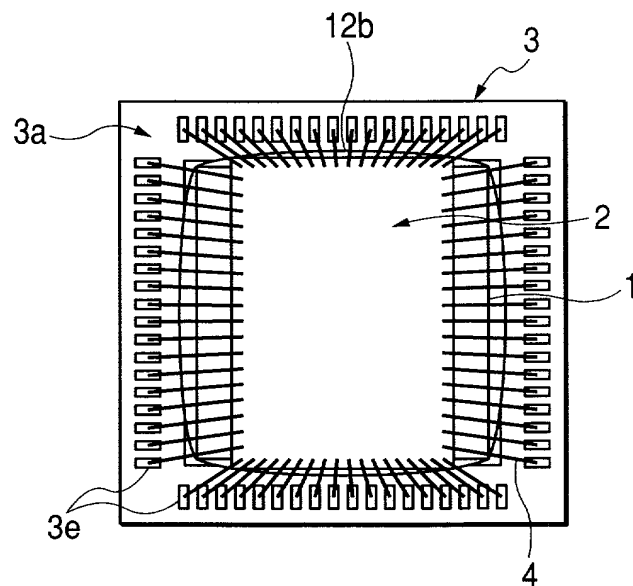


FIG. 26(b)



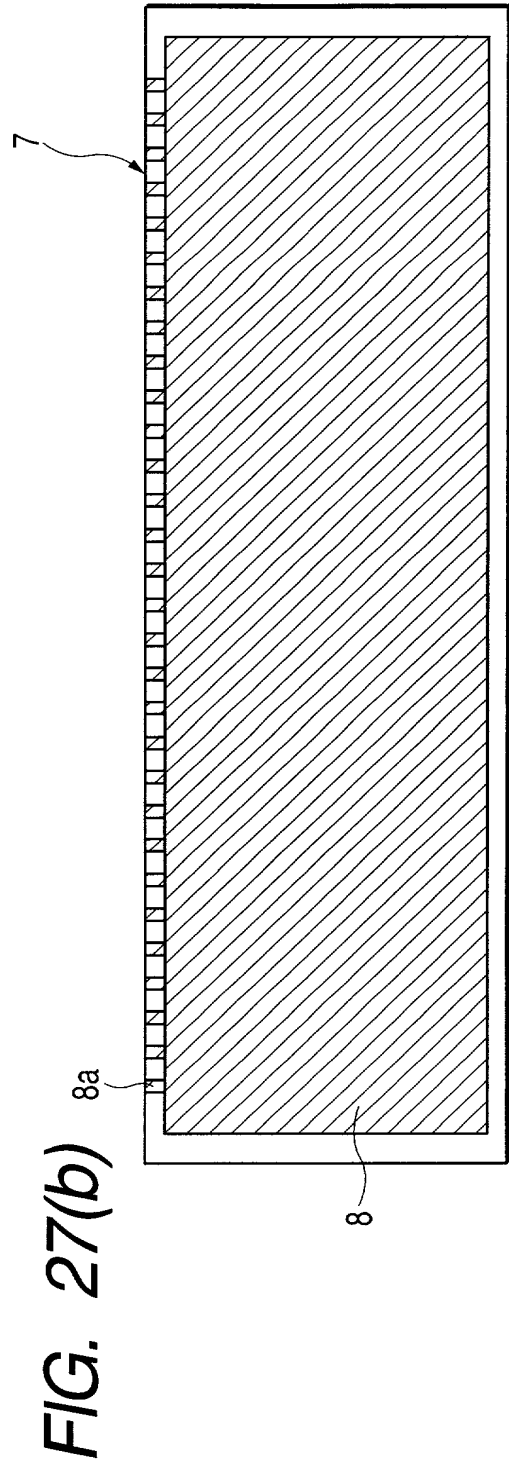
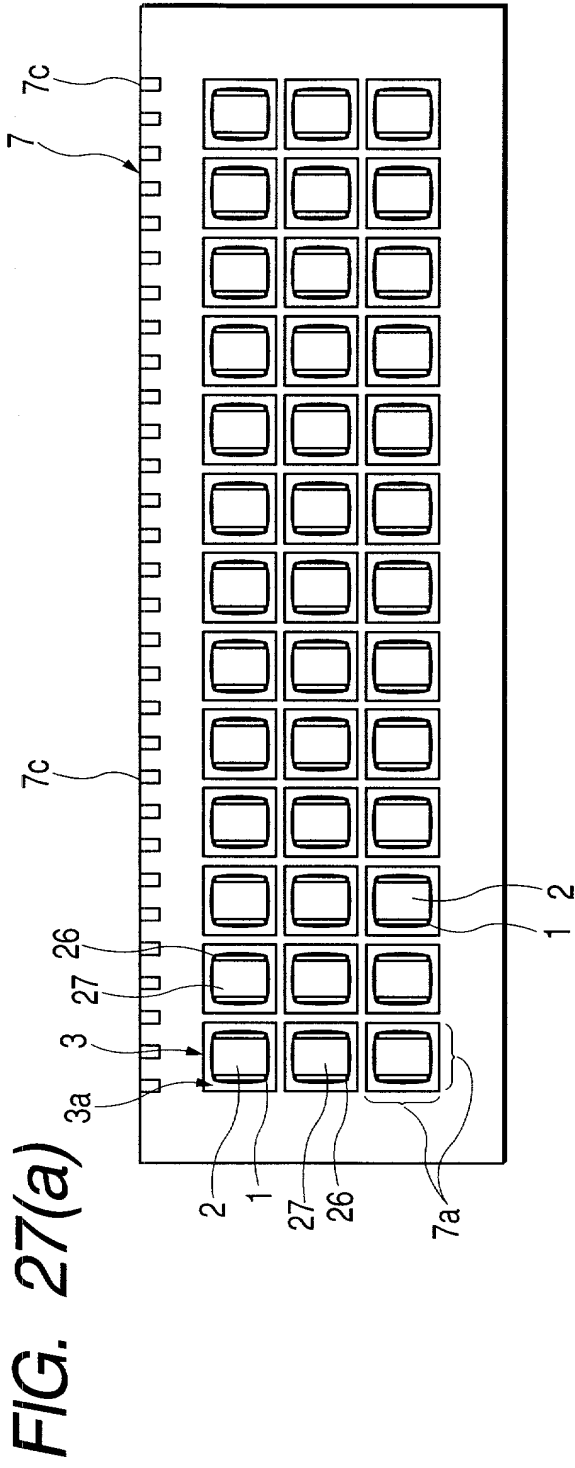
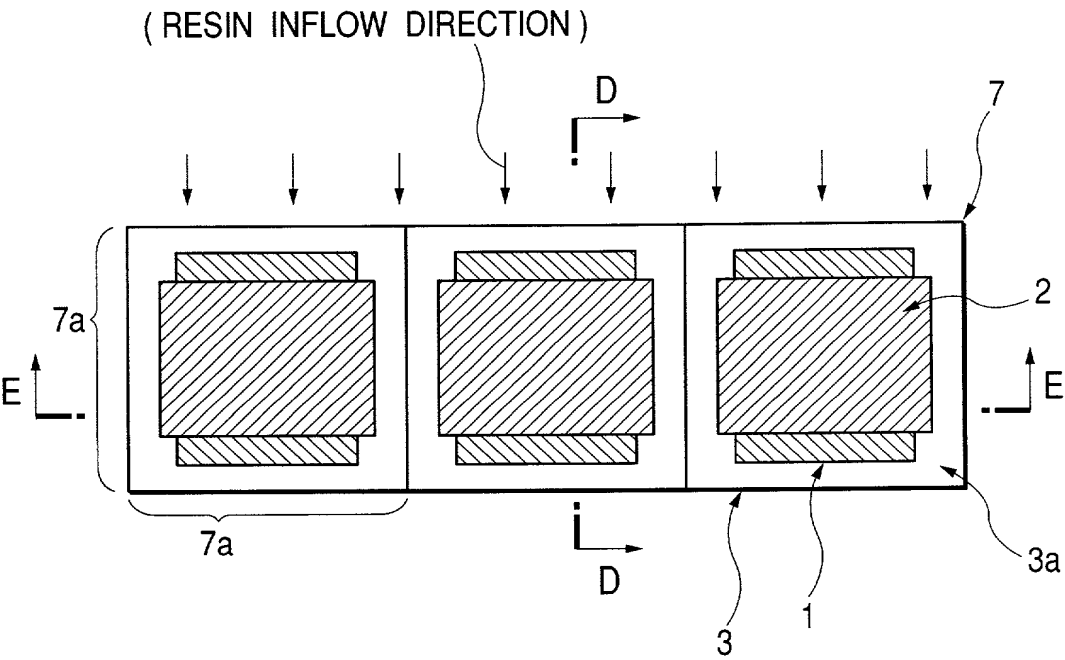
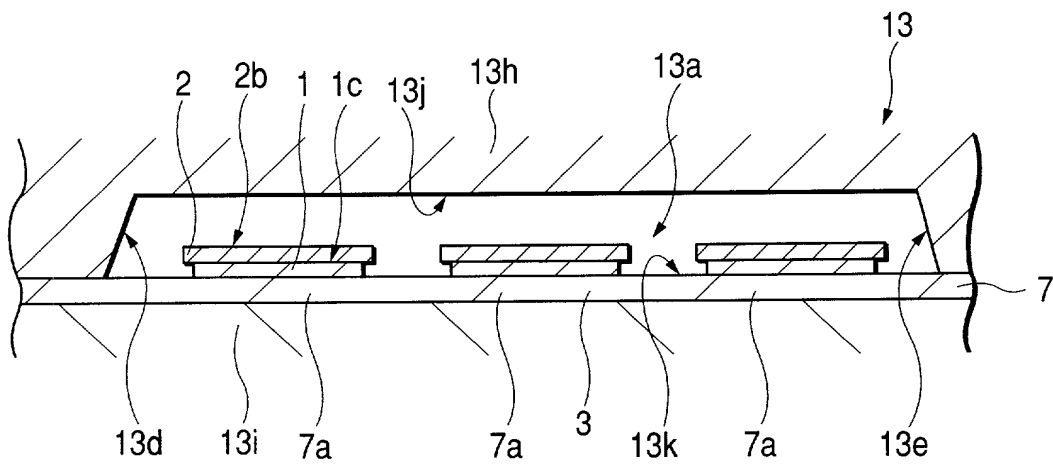


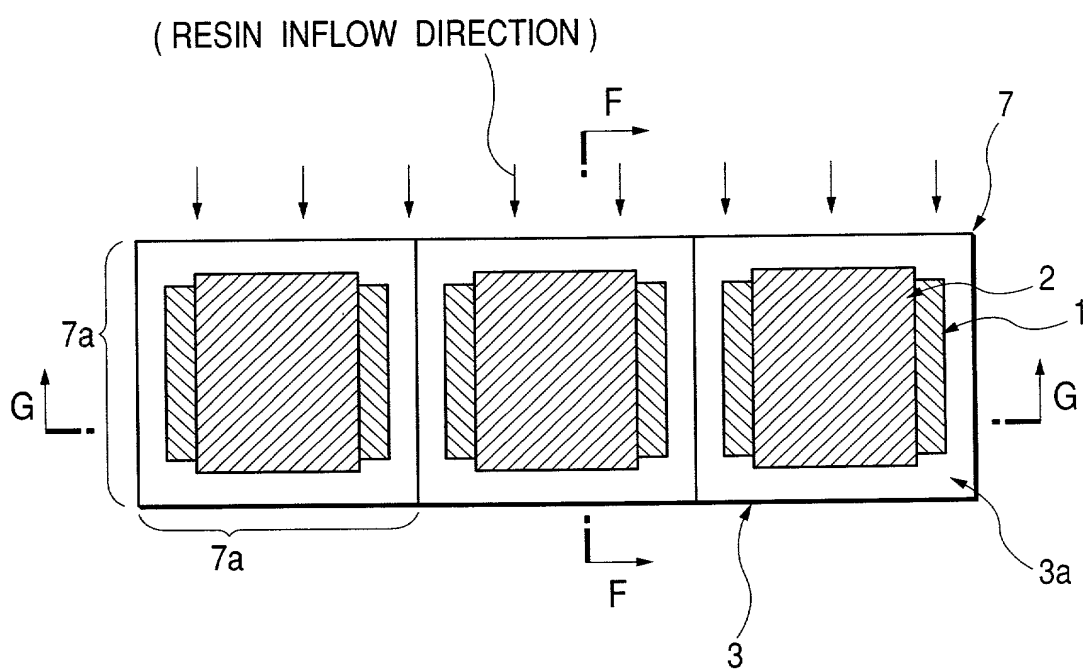
FIG. 28



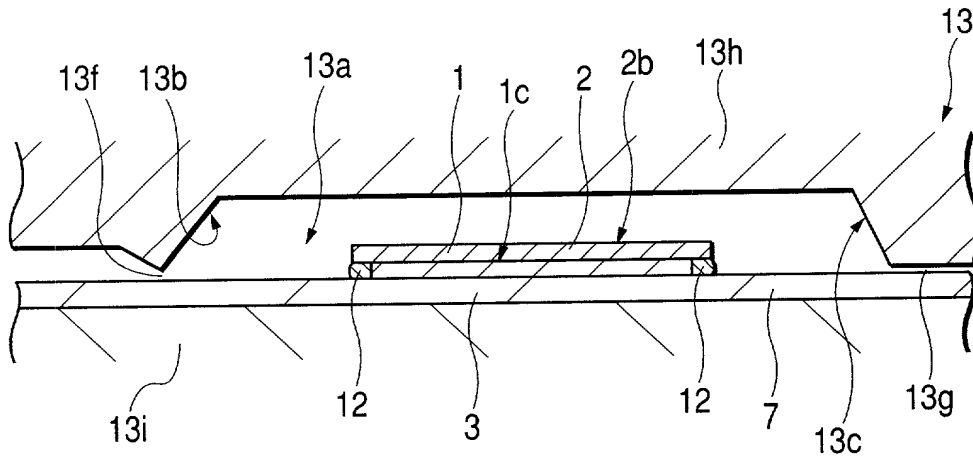


*FIG. 29(b)*



**FIG. 30**

**FIG. 31(a)**



**FIG. 31(b)**

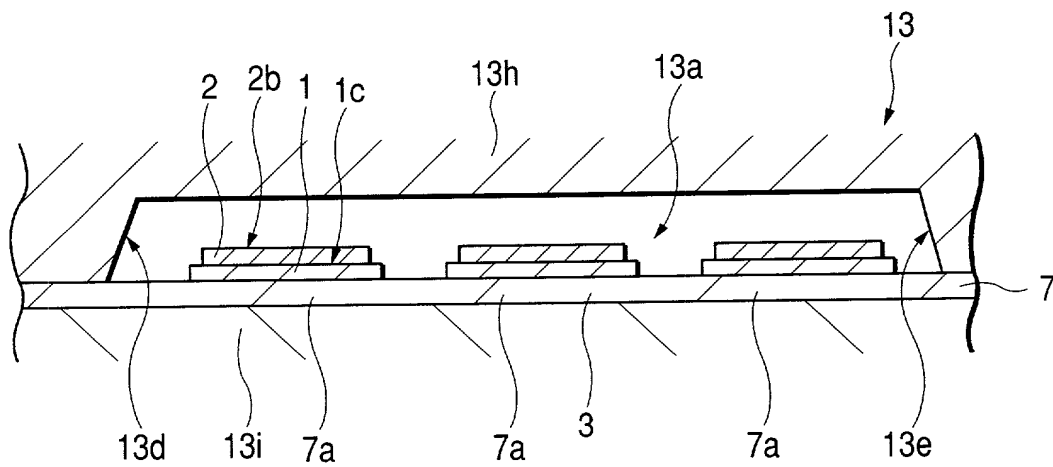


FIG. 32

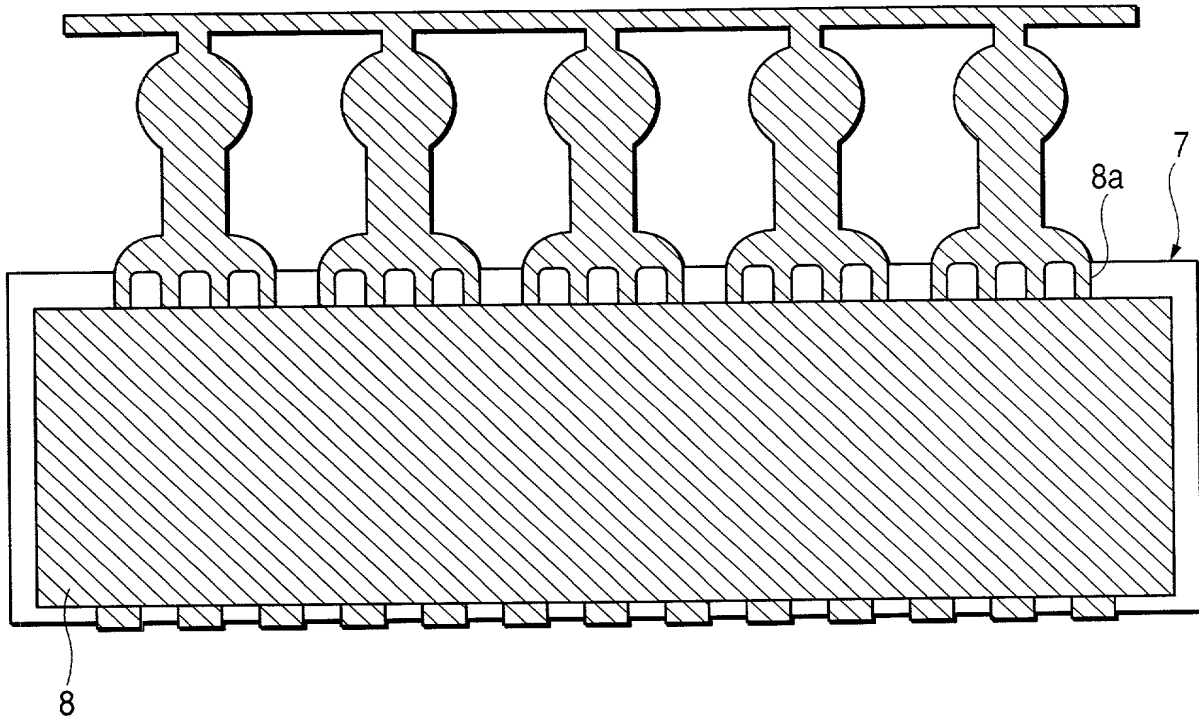


FIG. 33

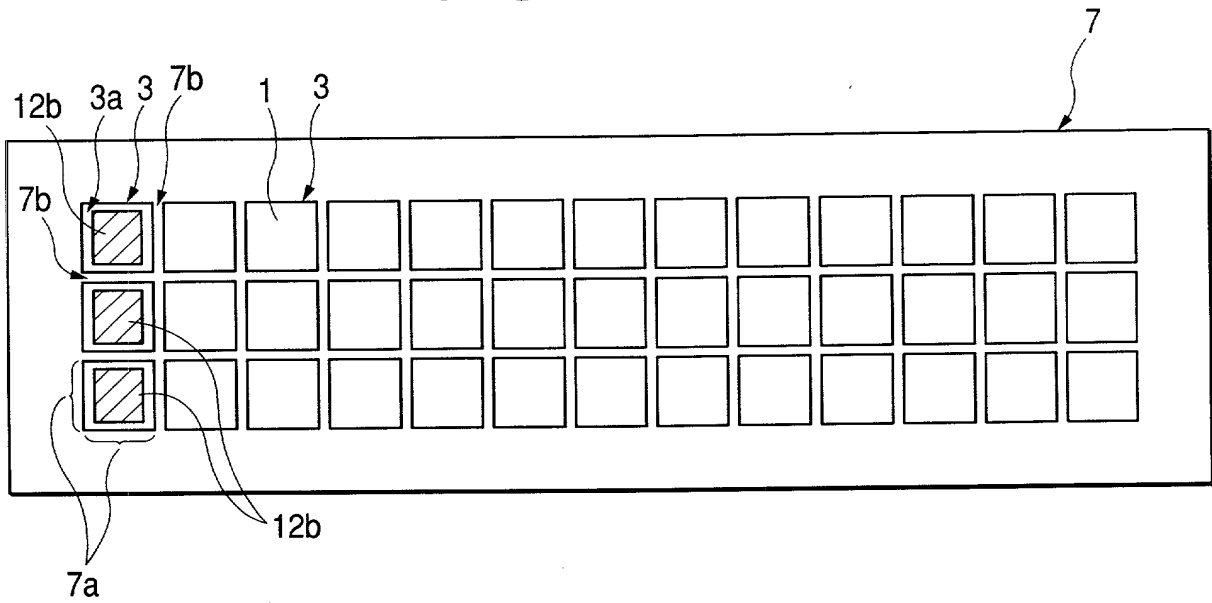


FIG. 34

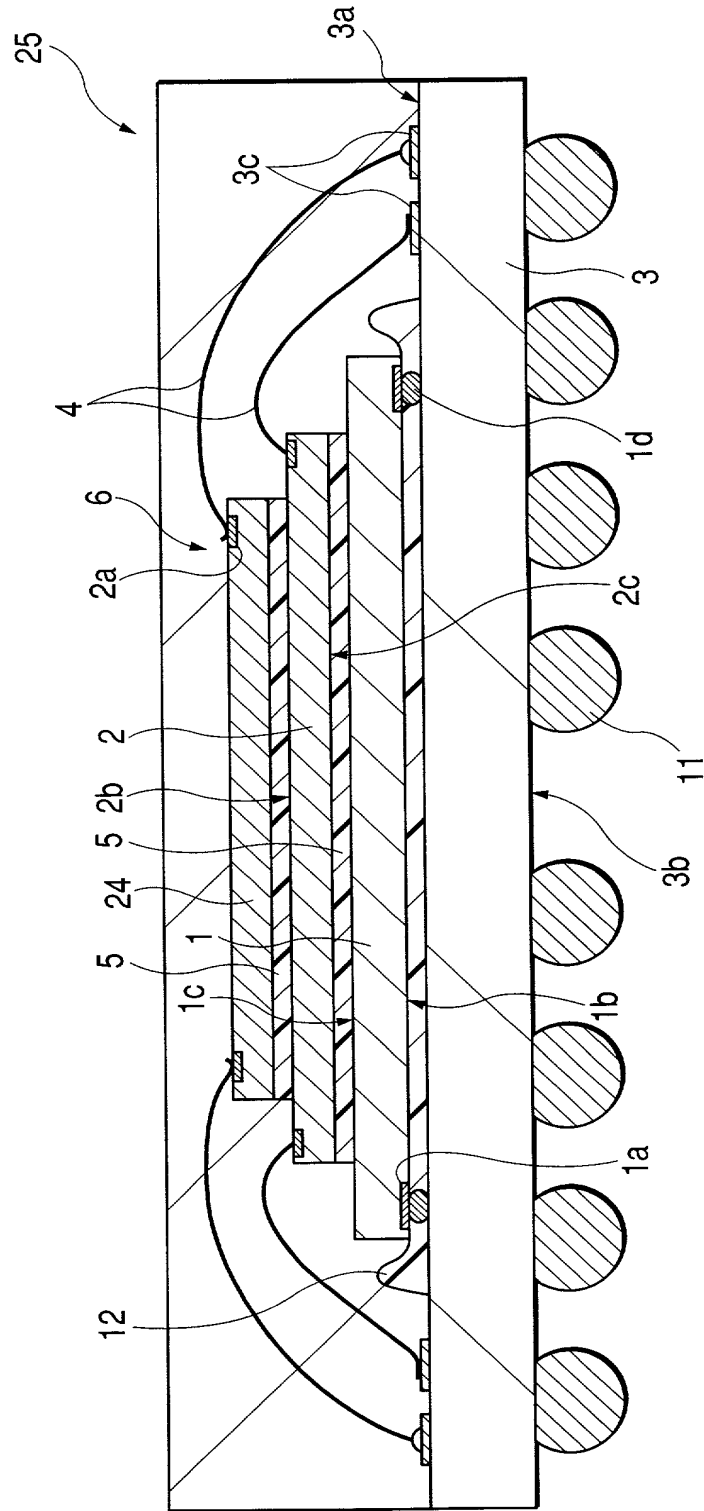


FIG. 35

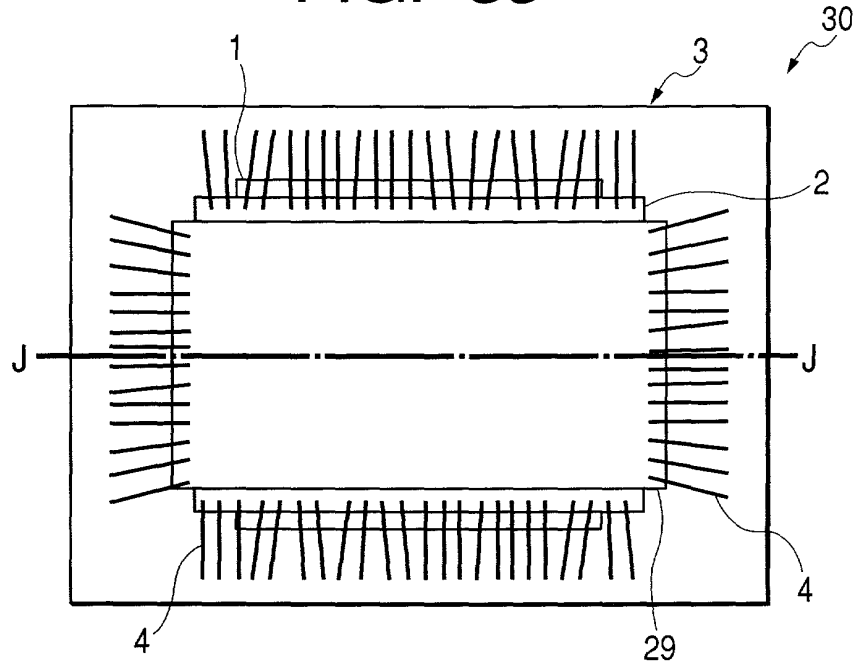
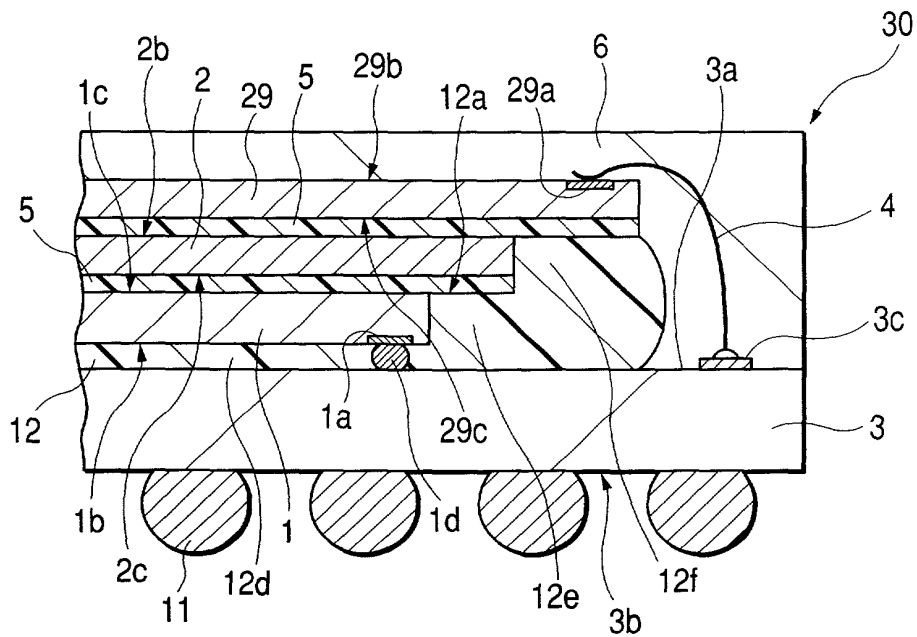


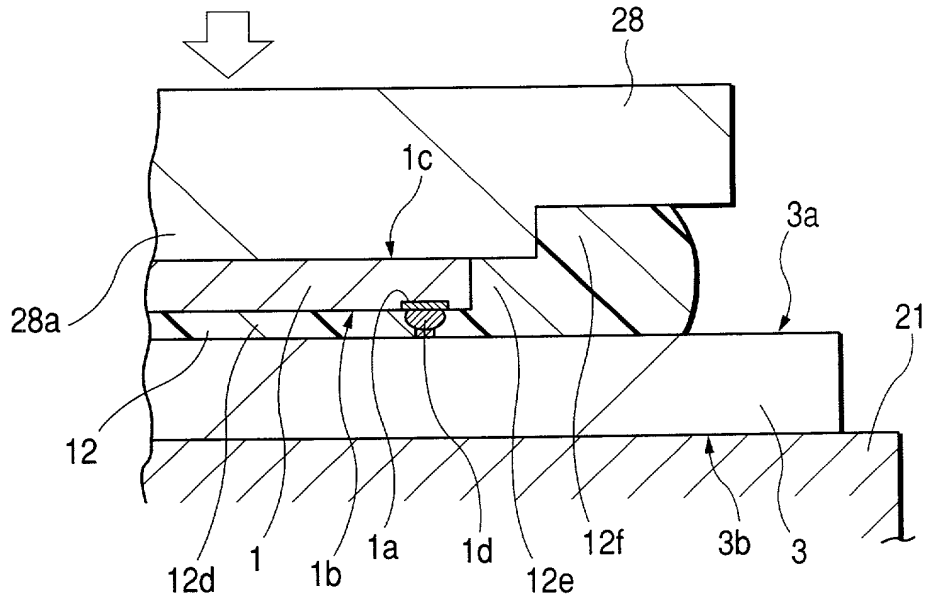
FIG. 36



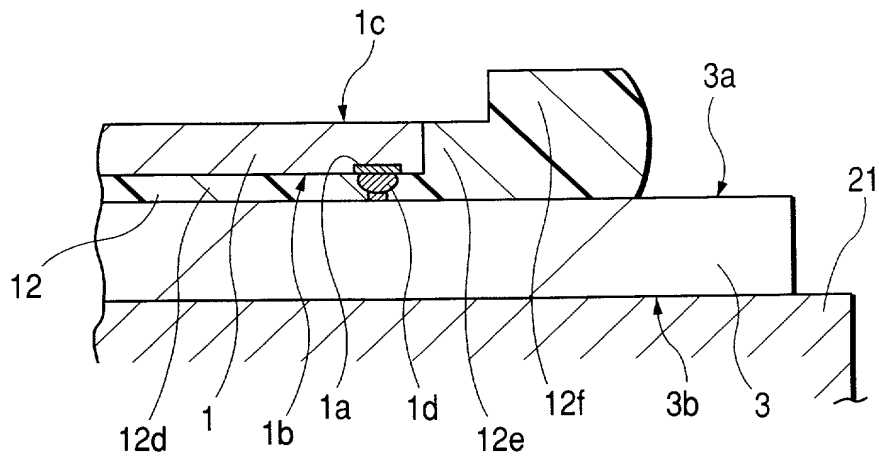
A diagram of a square frame structure, labeled 1. The structure is composed of a grid of circular elements. The top edge of the frame is labeled 1a, and the right edge is labeled 1b. The entire frame is labeled 1.

A cross-sectional view of a semiconductor device. A substrate 3 is shown with a layer 12 on its top surface. A patterned layer 1 is formed on layer 12, with a central portion 1a being a different material or structure. A contact 1d is formed on top of portion 1a. Labels 1b, 1c, and 1d point to different parts of the patterned layer 1. Labels 3a and 3b point to the substrate 3, and 3c points to the layer 12.

**FIG. 39**



**FIG. 40**





**FIG. 42**

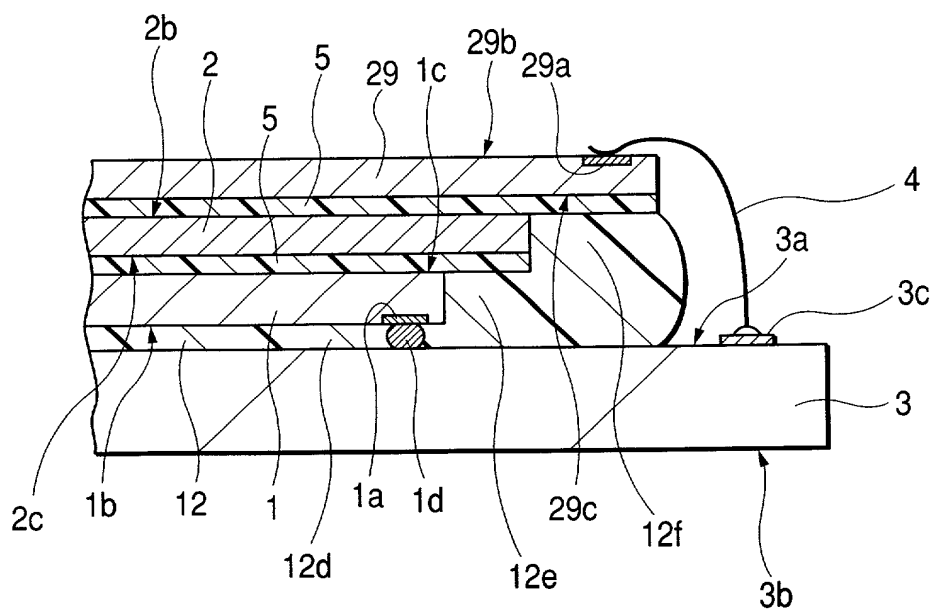


FIG. 43

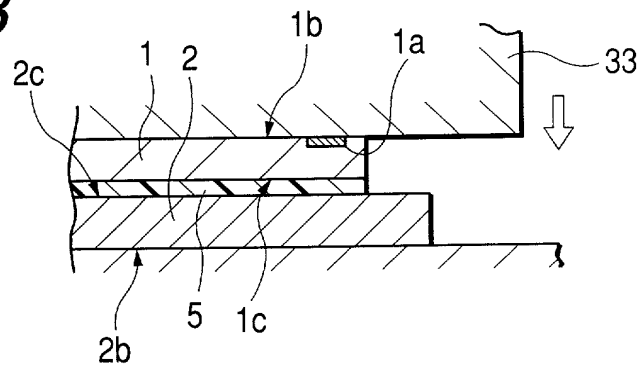


FIG. 44

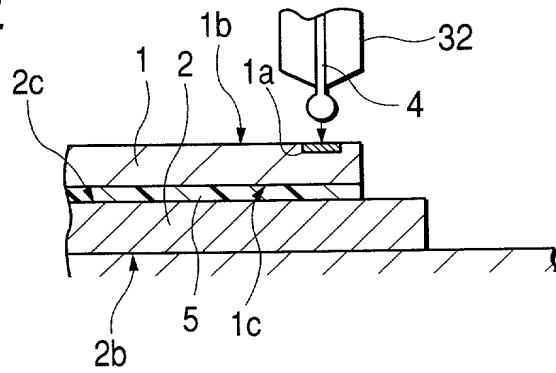
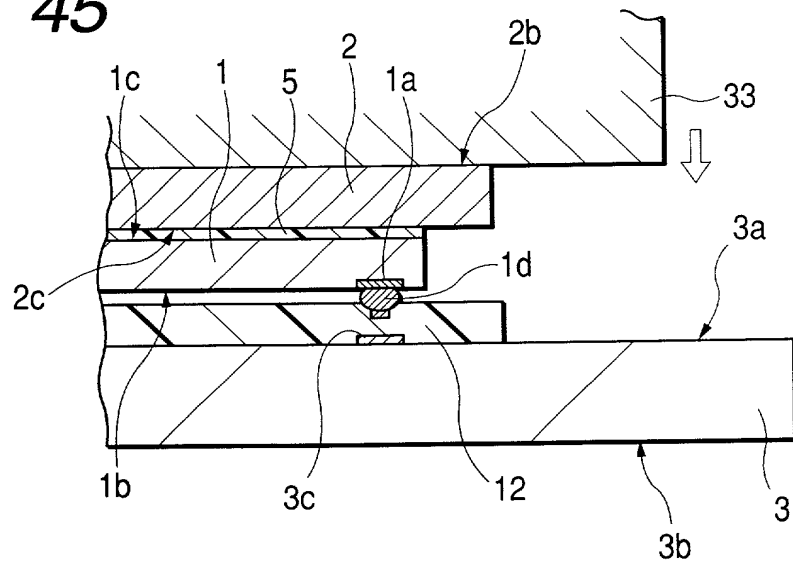


FIG. 45



**FIG. 47**

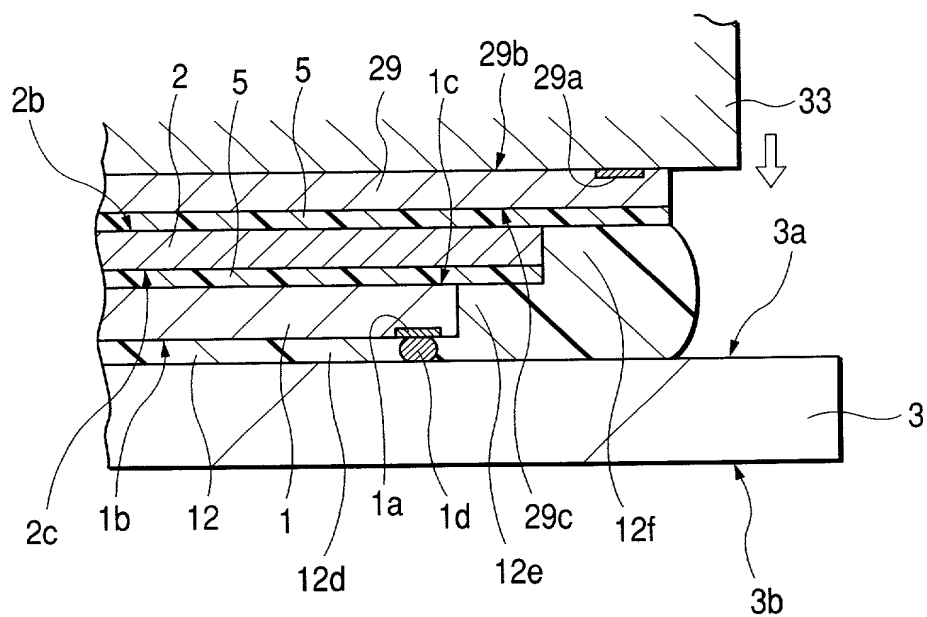


FIG. 48

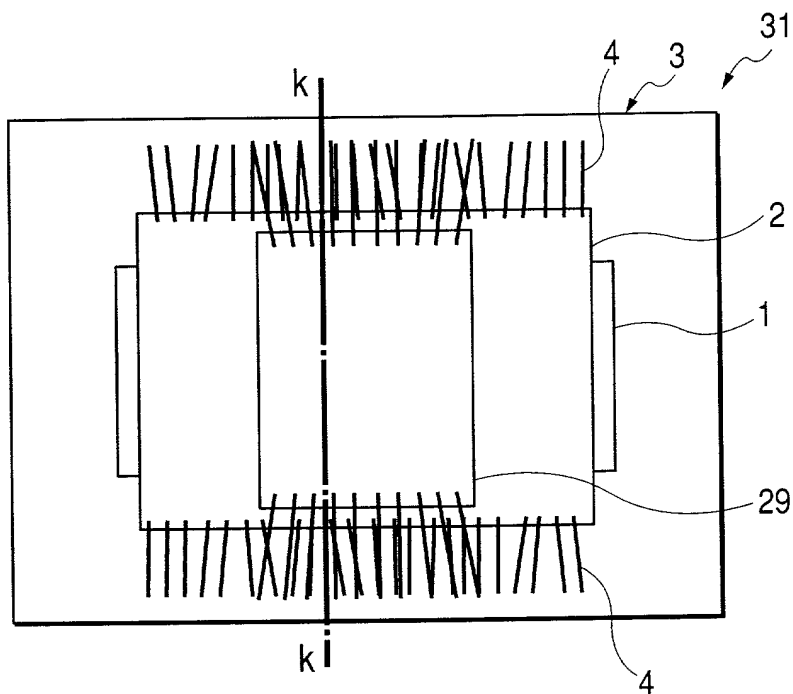
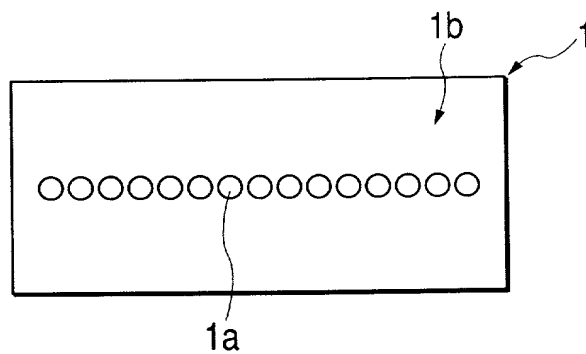
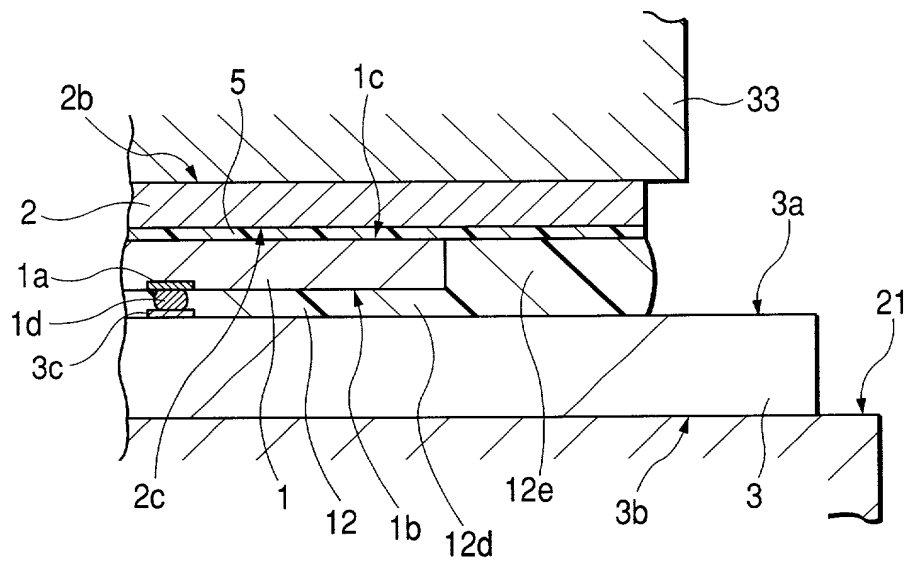


FIG. 49



**FIG. 51**



**FIG. 53**

